

# VERTIV<sup>™</sup> Albér UXCM Firmware Release Notes

# VERSION 3.4.8.7 (MARCH 17, 2017)

#### **Release Notes Section Outline**

- 1 Updates for This Release (Version 3.4.8.7)
- 2 Version 3.2.7.11 Update Information
- 3 Version 3.1.7.14 Update Information

## 1 Updates for This Release (Version 3.4.8.7)

- Float current hysteresis changed from 200mA to 20mA, resolution is 10 mA.
- Monitor watchdog changed from 60 to 90 seconds (potential timeout issue when all UXBM-50 devices offline).
- Increased maximum number of inter-tiers from 64 to 96.
- Disabled String cell voltage and low overall voltage alarms during discharge.
- Resistance test delay and alarm delay after discharge changed from 8 hours to 24 hours.
- Added string current resolution limited to 5 amps. Anything below will be set to zero. Use float current for measurements under 5 amps.
- Added UXIMe identifier.
- Increased cell voltage hysteresis from 10 mV to 30 mV and added 1V hysteresis to overall voltage measurement reporting.
- Firmware upgrade of UXBM-50 is supported when present and in bootloader mode.

#### 2 Version 3.2.7.11 Update Information

- Added USB connectivity enhancements.
- UXBM-50 configuration information now loads for first UXBM-50 after power up.
- Added resistance test serialization by string. Only one string runs resistance test at a time.
- Discharge normalization changed from 72 hours to 8 hours.
- Thermal runaway alarm now needs to have all trigger events present before setting the alarm.
- Thermal runaway Charger Level alarms disabled during discharge normalization.
- Most recent inter-tier data for cabinet configurations is now loaded during start-up.

## 3 Version 3.1.7.14 Update Information

- The last stored resistance history data is now loaded into the current resistance view (rather than the oldest resistance history data).
- Resolved the clearing of some discharge alarms.

© 2017 Vertiv Co. All rights reserved. Vertiv and the Vertiv logo are trademarks or registered trademarks of Vertiv Co. All other names and logos referred to are trade names, trademarks or registered trademarks of their respective owners. While every precaution has been taken to ensure accuracy and completeness herein, Vertiv Co. assumes no responsibility, and disclaims all liability, for damages resulting from use of this information or for any errors or omissions. Specifications are subject to change without notice.